

FIG.1 A

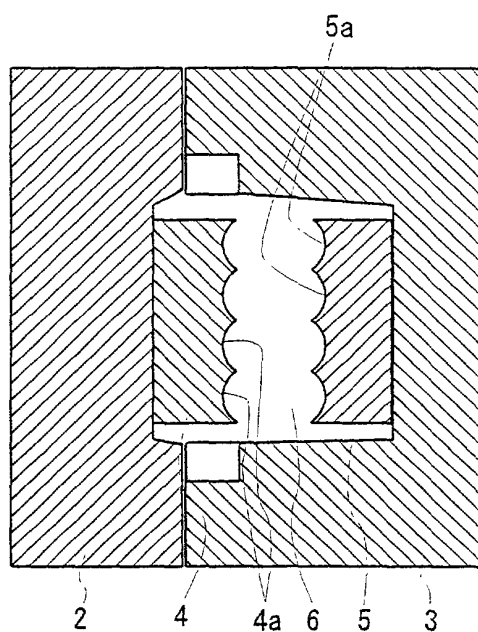


FIG.1 B

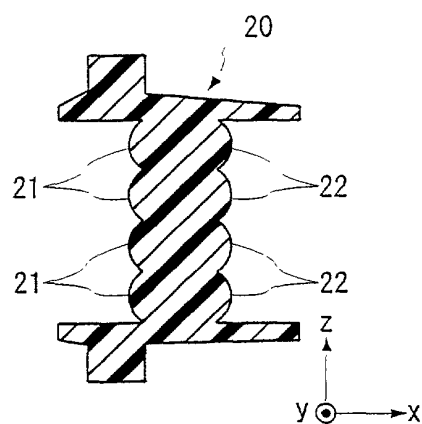


FIG.2

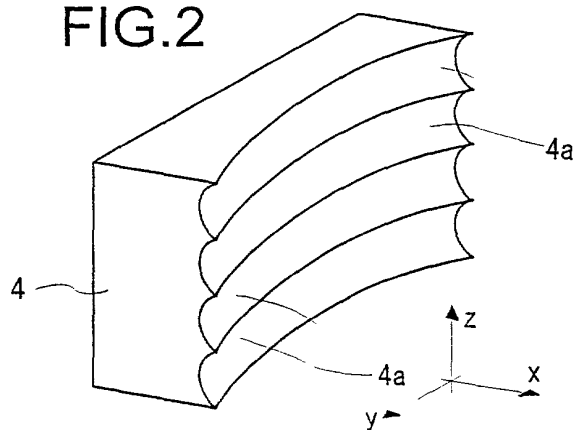


FIG. 3 is a cross-sectional view of a device in accordance with the present invention. The device includes a substrate 1, a first layer 2, a second layer 3, a third layer 4, a fourth layer 5, a fifth layer 6, and a sixth layer 7. The device is shown in a cross-sectional view, with the layers 2, 3, 4, 5, 6, and 7 being stacked on top of each other. The substrate 1 is at the bottom. The layers 2, 3, 4, 5, 6, and 7 are shown with different hatching patterns to indicate they are different materials. The device is shown in a cross-sectional view, with the layers 2, 3, 4, 5, 6, and 7 being stacked on top of each other. The substrate 1 is at the bottom. The layers 2, 3, 4, 5, 6, and 7 are shown with different hatching patterns to indicate they are different materials.

FIG. 3

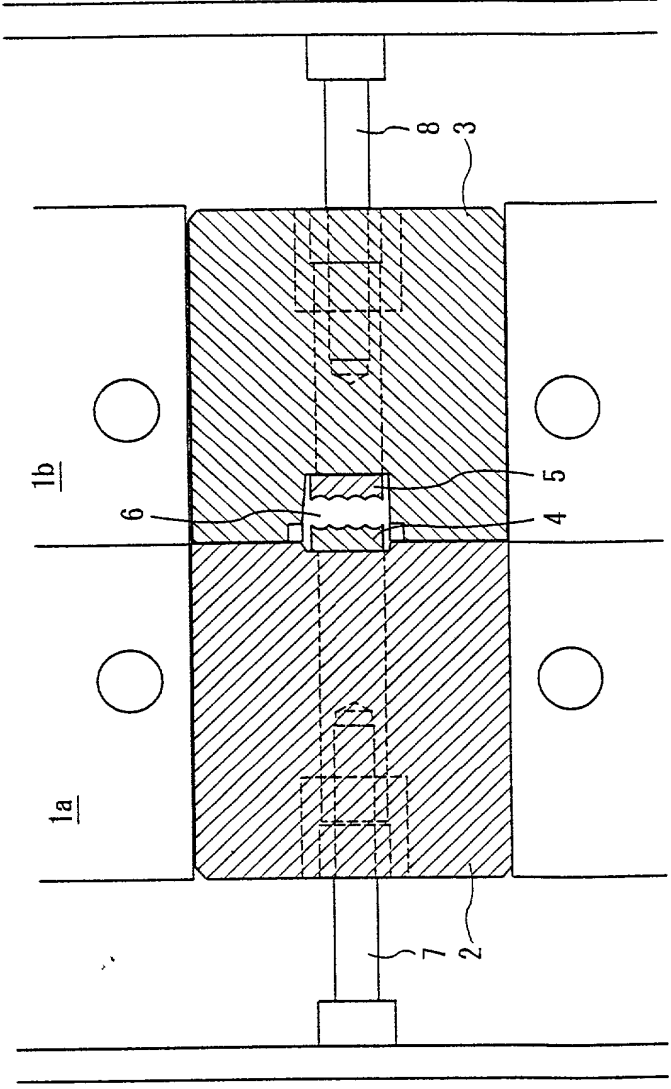


FIG.4

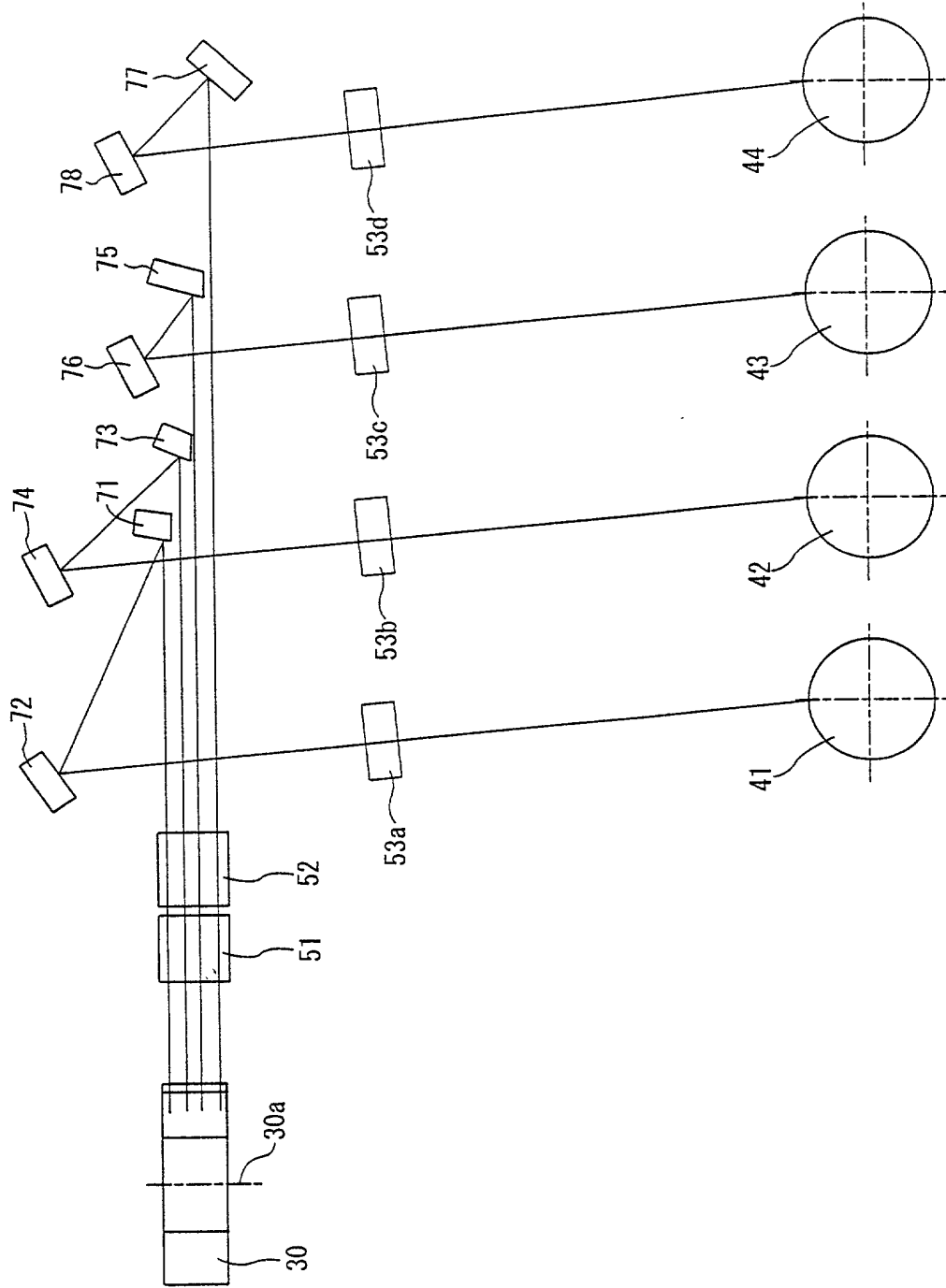


FIG.5

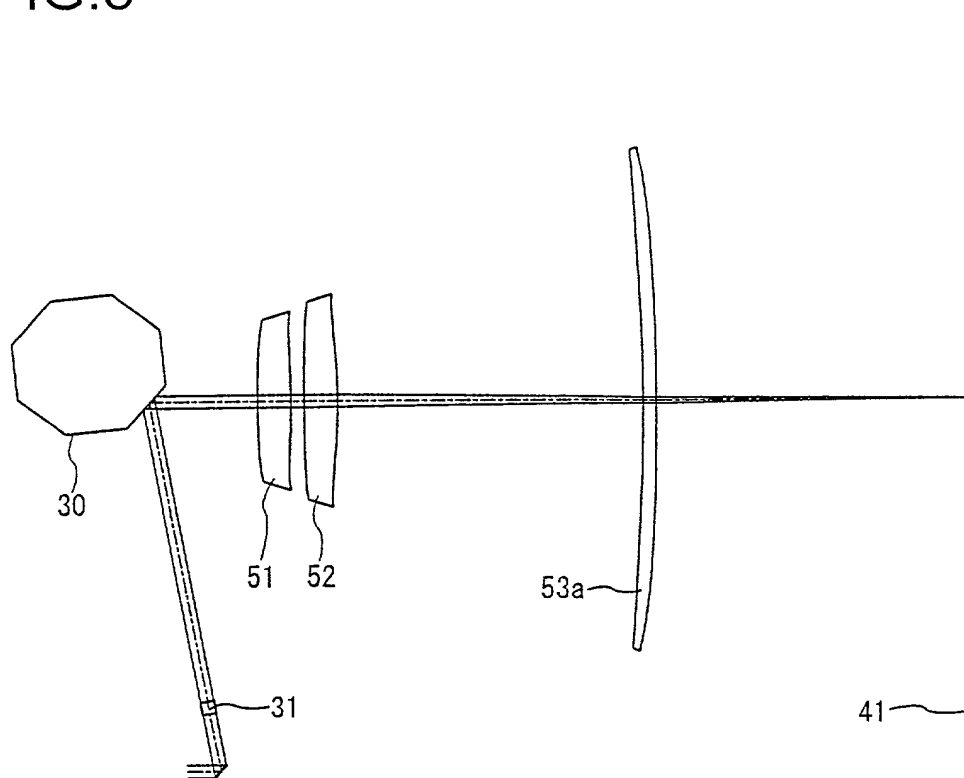


FIG.6

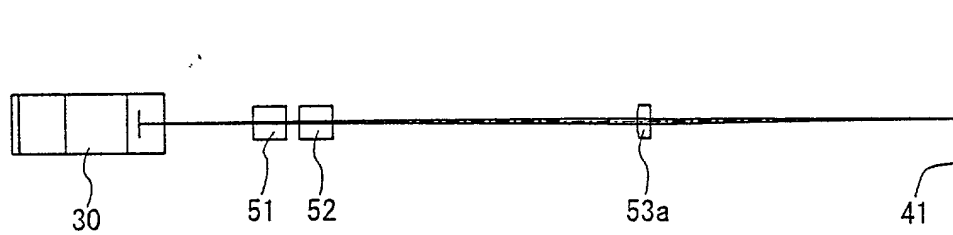
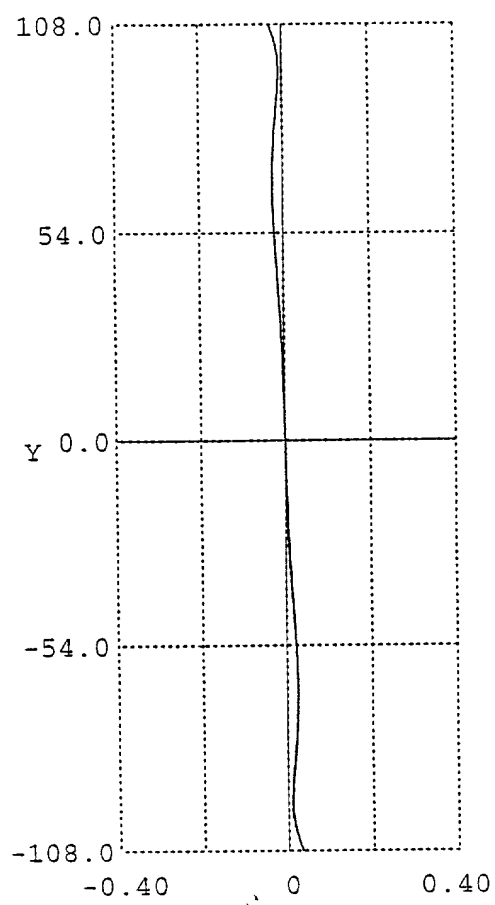
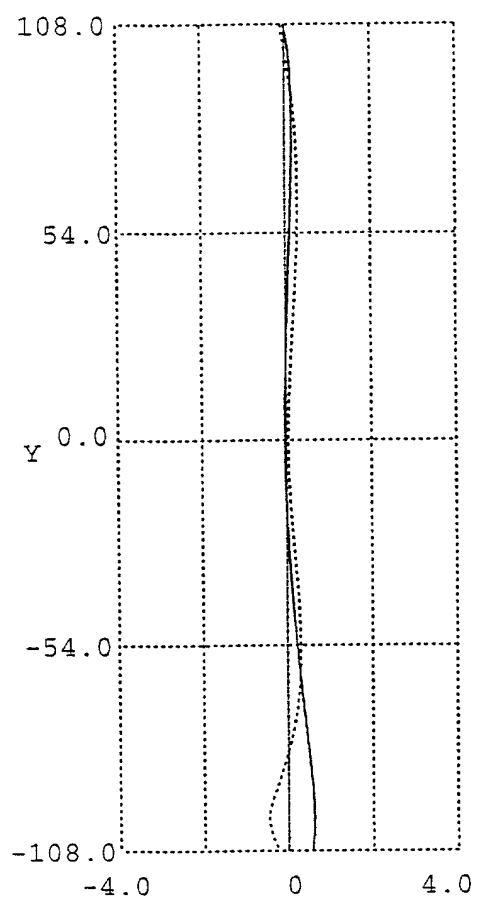


FIG.7A



LINEARITY ERROR

FIG.7B



CURVATURE OF FIELD

..... MAIN SCANNING DIRECTION
 ——— AUXILIARY SCANNING DIRECTION

FIG.8

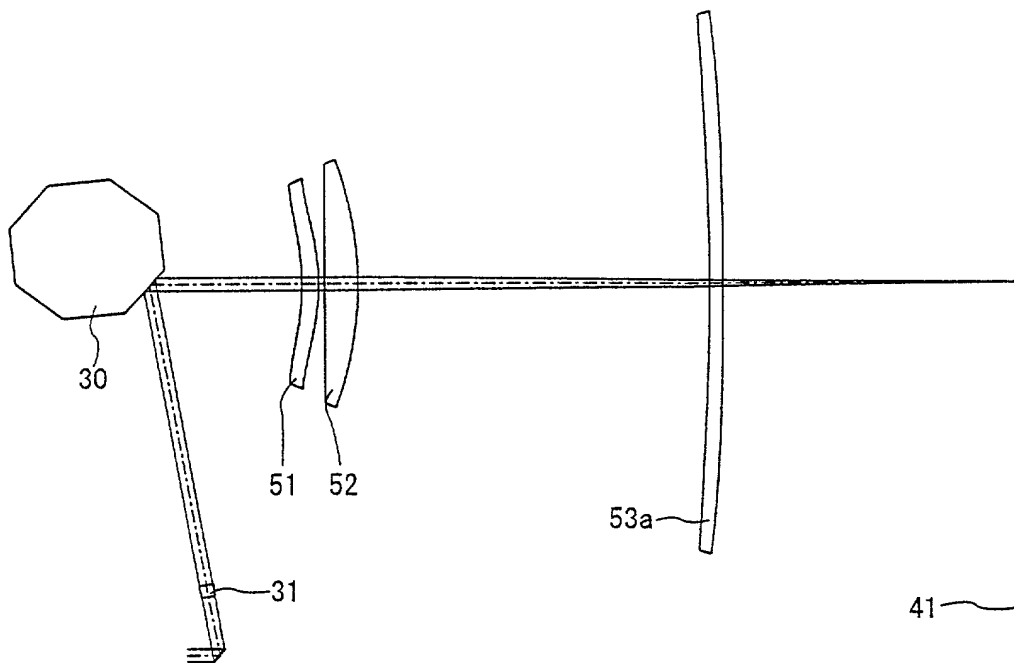


FIG.9



FIG.10A

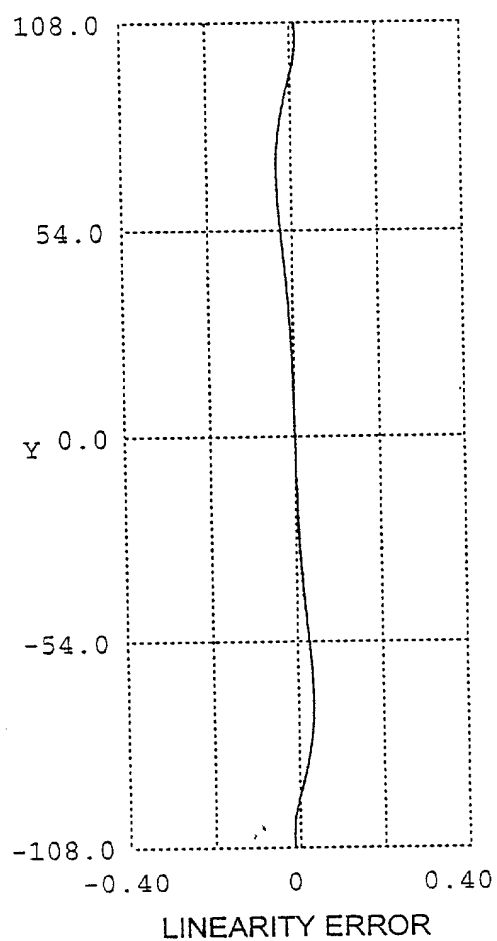
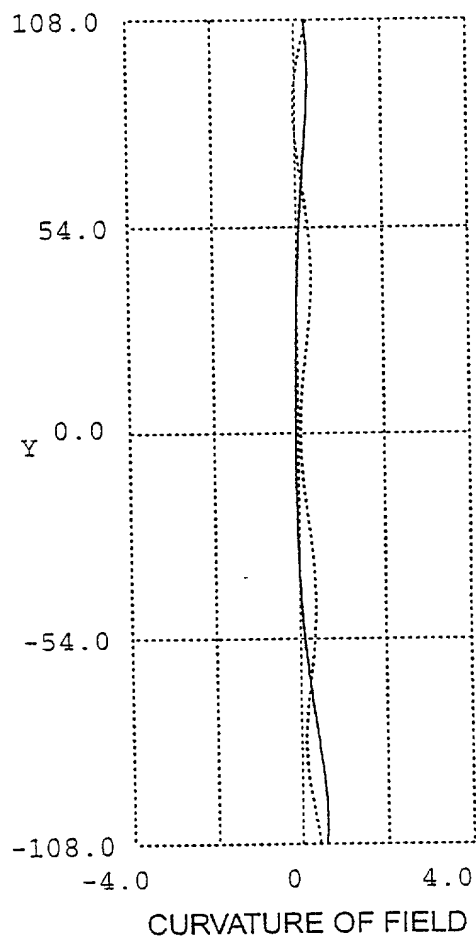


FIG.10B



..... MAIN SCANNING DIRECTION
 —— AUXILIARY SCANNING DIRECTION